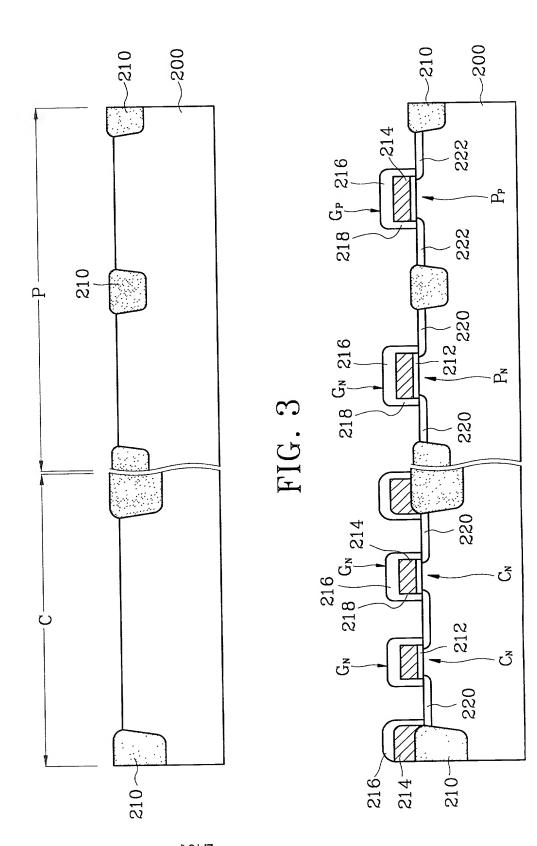


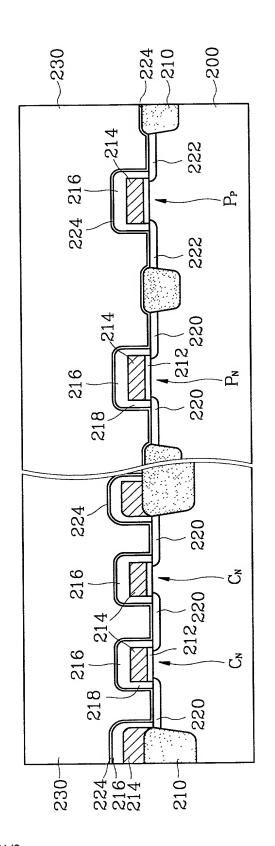
Attorney Docket No. 9898-201/Application No.



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Attorney Docket No. 9898-201/Application No.

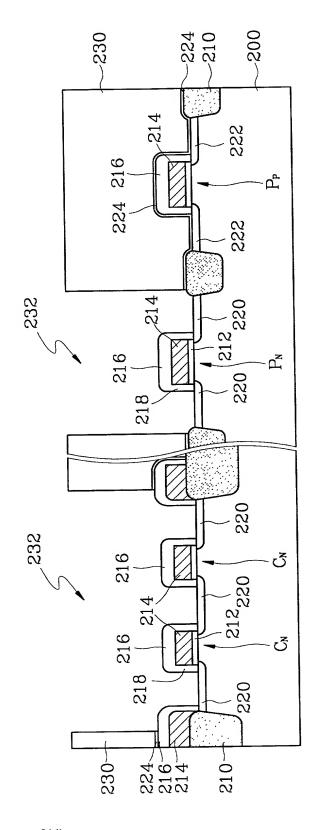
WELHOD OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT KANGE CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT



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WEITHOD OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT REGION OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT AND OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT

FIG. 5

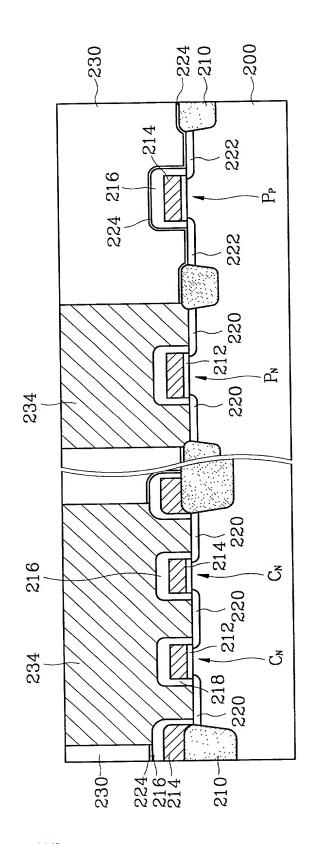


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 $\mbox{AREA} \label{eq:AREA} \mbox{Attorney Docket No. 9898-201/Application No.}$

WELHOD OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT RAING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT

FIG. 6

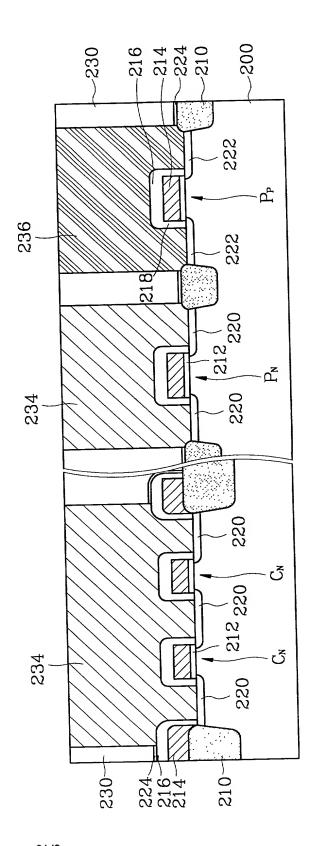


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AREA Attorney Docket No. 9898-201/Application No.

WETHOD OF PORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT APPLY OF PORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT

FIG. 7



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WETHOD OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT

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FIG. 8

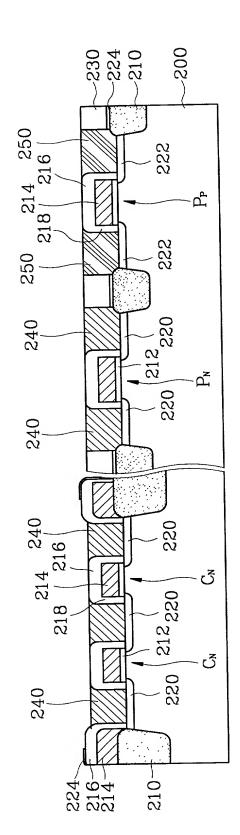
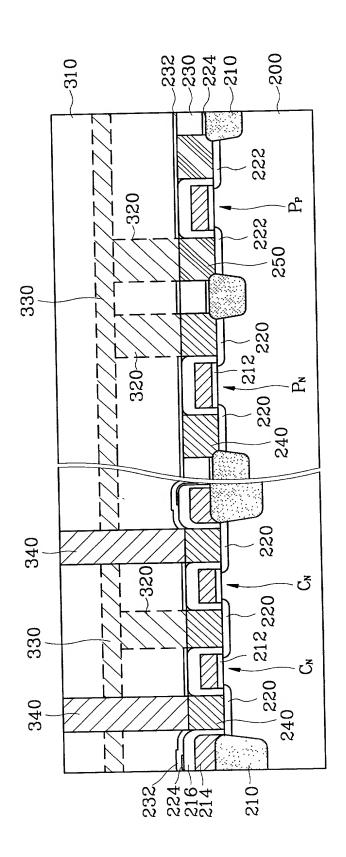


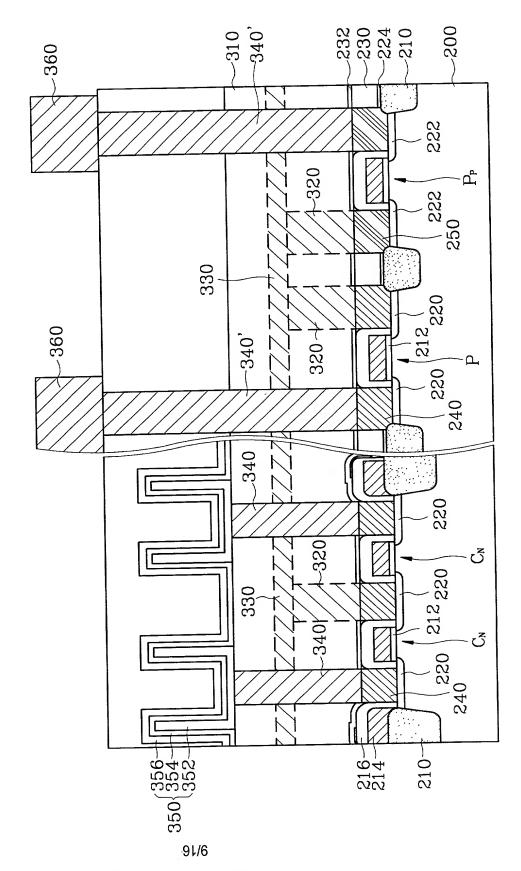
FIG. 9



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WETHOD OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT KANNO OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT

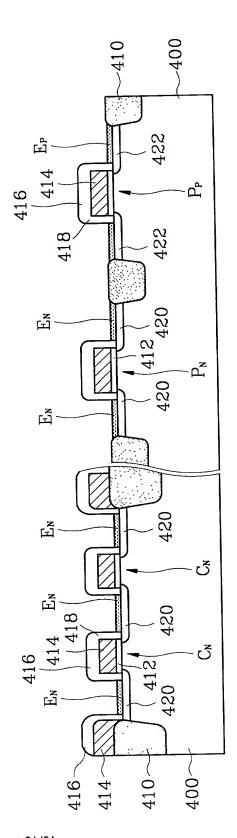
FIG. 10



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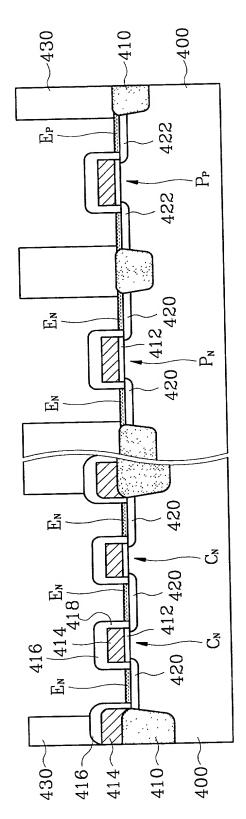
WELHOD OF FORMING SEMICONDUCTOR DEVICE HAVING CONTACT PAD ON SOURCE/DRAIN REGION IN PERIPHERAL CIRCUIT Kang-Yoon LEE

FIG. 11



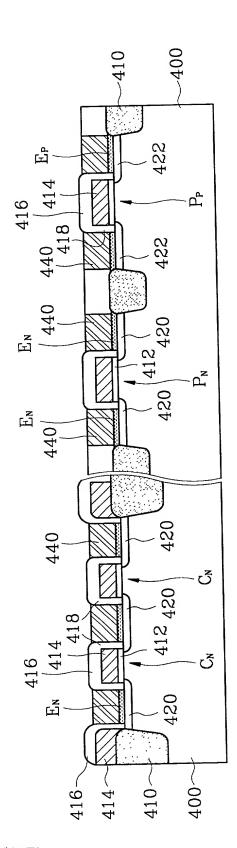
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FIG. 12



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FIG. 13



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FIG. 14A

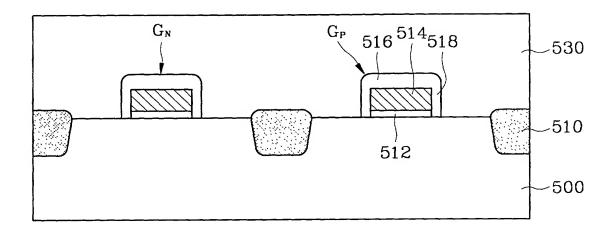
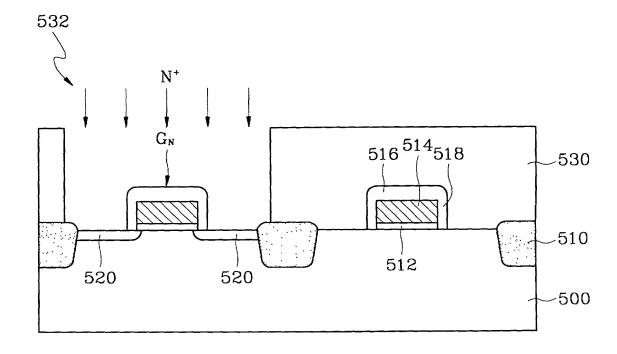


FIG. 14B



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FIG. 14C

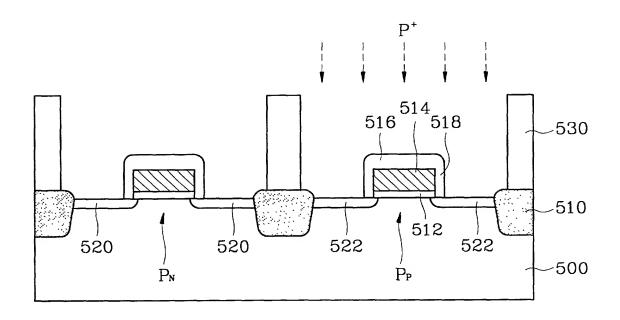
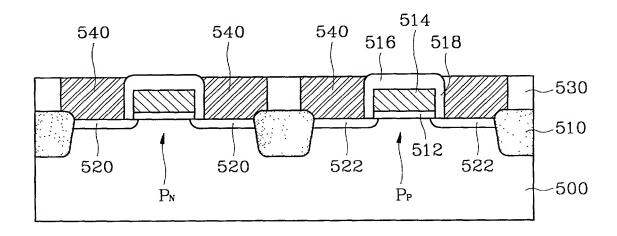


FIG. 14D



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FIG. 15A

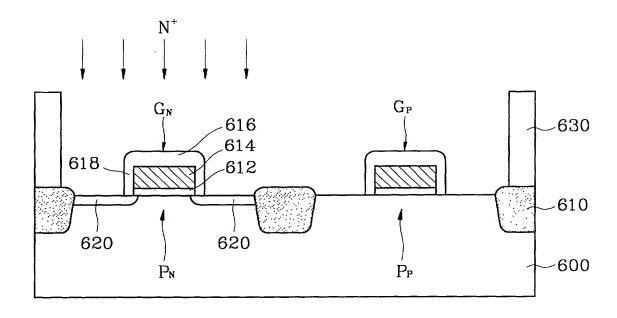
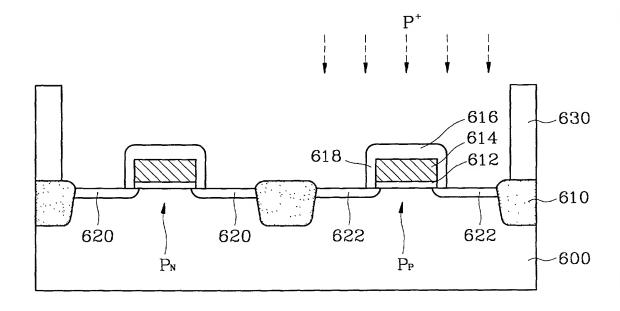


FIG. 15B



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FIG. 15C

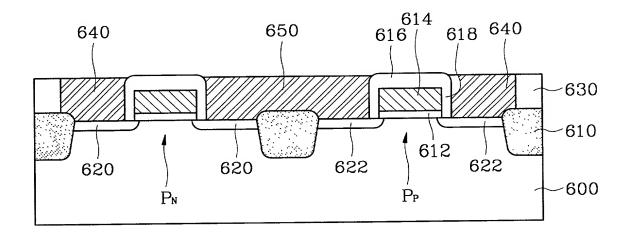
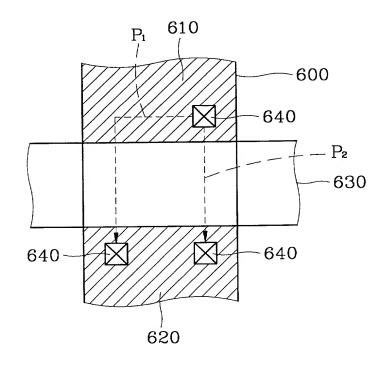


FIG. 16 (PRIOR ART)



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